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Docket No.: M4065.0767/P767
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Barmak Mansoorian

Application No.: 09/359,056

Art Unit: 2615

Filed: July 21, 1999

Examiner: N. T. Tran

For: HIGH RESOLUTION CMOS CIRCUIT
USING A MATCHED IMPEDANCE
OUTPUT TRANSMISSION LINE

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Technology Center 2600

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated May 7, 2004 (Paper No. 21), please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

08/09/2004 SZEWDIE1 00000046 09359056

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